

#### REMARKS

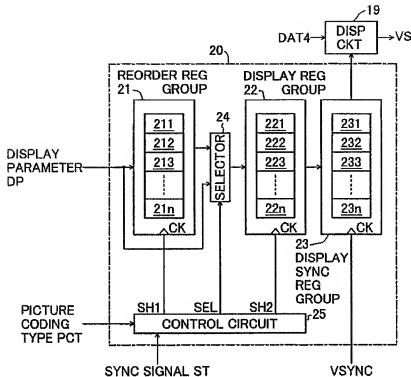
Claims 1-11, and 16 are presently pending. Claims 12-15 are cancelled without prejudice.

Claims 1-11 were rejected under 35 U.S.C. § 103(a) as being obvious from the combination of Kono and Miyawaki. Claim 1 is amended to recite, among other limitations, "a FIFO for storing indicators indicating images to be displayed". Examiner has indicated that "Kono does not particularly teach a queue comprises a FIFO for storing indicators indicating images to be displayed". Office Action, at 3. However, Examiner indicated that "Miyaki teaches a queue (20 of fig. 1) comprises a FIFO (20 of fig. 2)". Office Action at 3. Assignee respectfully traverses and calls Examiner's attention to Col. 5, Lines 11-30 and Figure 2, reproduced below.

Control circuit 25 judges the code of picture coding type PCT in synchronization with signal ST. In a case where control circuit 25 judges that the PCT indicates I-picture or P-picture (reference picture), selector 24 is caused to select the output of register group 21, whereby the outputs of registers 211 to 21n are simultaneously caused to be provided into registers 221 to 22n. Next, latch signal SH2 is made active and output of register group 21 is stored in register group 22, and subsequently latch signal SH1 is made active and display parameters DP are stored in registers 211 to 22n of register group 21. ...

In a case where control circuit 25 judges that the PCT indicates B-picture (non-reference picture), selector 24 is caused to select display parameters DP, thereby causing DP to be provided into registers 221 to 22n, wherein latch signal SH2 is made active to cause DP to be stored in register group 22.

**FIG.2**



Since, "In a case where control circuit 25 judges that the PCT indicates I-picture or P-picture" "display parameters DP are stored in registers 211 to 22n of register group 21" (the left-most register group 21), and "In a case where control circuit 25 judges that the PCT indicates B-picture" "DP to be stored in register group 22" (the center register group 23), it is respectfully submitted that display parameter synchronous holding circuit 20 does not store on a "First-In-First-Out" basis. Accordingly, the combination of Kono and Miyakawi do not teach "a FIFO for storing indicators indicating images to be displayed", "queuing indicators indicating images to be displayed in a FIFO" (Claim 5), "storing indicators indicating images to be displayed in a FIFO" (Claim 8), and "storing indicators indicating images to be displayed in a FIFO" (Claim 11). Accordingly, Examiner is respectfully requested to withdraw

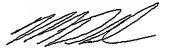
the rejection to claims 1, 5, 8, and 11, as well as to dependent claims 2-4, 6, 7, 9, and 16.

#### CONCLUSION

For at least the foregoing reasons, Assignee submits that each of the pending claims are now in a condition for allowance. Accordingly, Examiner is requested to pass this case to issuance.

It is believed that all monies for the actions described herein are provided with this correspondence. To the extent that additional monies are required for any of the actions requested in the correspondence, Commissioner is authorized to charge such fees and credit any overpayments to deposit account 13-0017.

Respectfully Submitted



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